

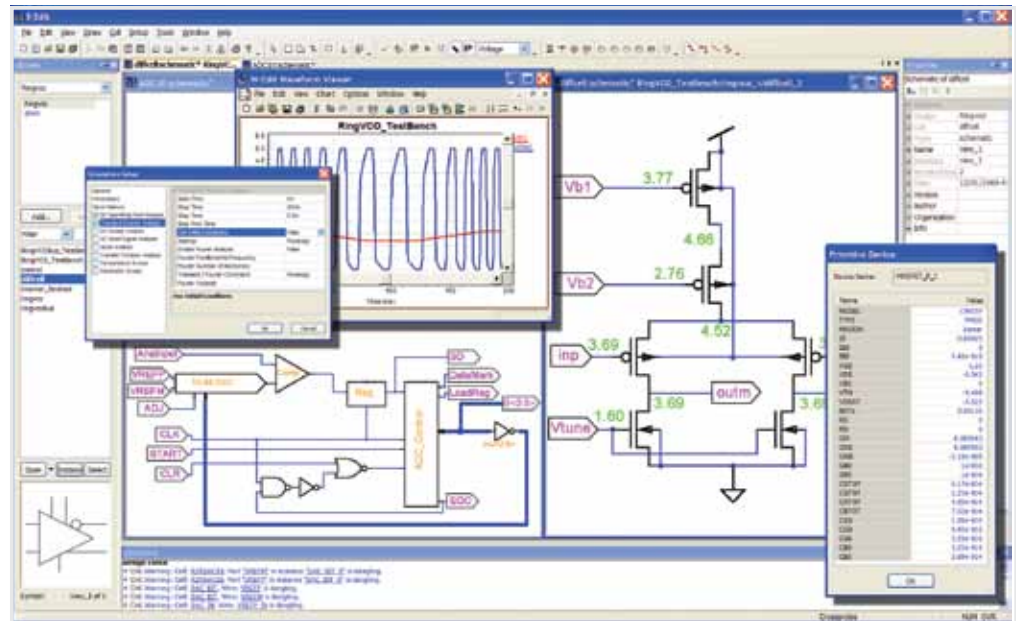
## S-EDIT FOR SCHEMATIC CAPTURE

### Product Benefits

- Handles your most complex full custom IC schematic capture.
- Speeds up design cycles through tight integration with simulation.
- Offers easy interoperability with third-party tools and legacy data.
- Simplifies schematic capture through a powerful, easy-to-use interface.
- Delivers an ideal performance-to-cost ratio.

### Product Features

- Tight integration with SPICE simulation and waveform cross-probing.
- View operating point simulation results directly on the schematic.
- Export SPICE, EDIF, Verilog, and VHDL.
- Import EDIF with automatic conversion of Cadence®, Mentor®, Laker™, ORCAD® and ViewDraw® Schematics.
- Advanced array and bus support.
- 64-bit for increased capacity and higher performance.
- View evaluated results of any calculated parameters.
- Property callbacks for validation or advanced property dependencies.
- Multiple symbol and schematic views per cell including Verilog-A and SPICE text views.
- Multiple page schematics.
- Schematic ERC with configurable Design Checks.
- Multiple library support with integrated library browser.
- Auto symbol generation.
- Fully scriptable and expandable using TCL/Tk command language.
- Hierarchical Find and Replace.
- Quickly traverse your design hierarchy with the Hierarchy Navigator.



## Speeding Concept to Silicon

S-Edit™ is an easy-to-use PC-based design environment for schematic capture. It gives you the power you need to handle your most complex full custom IC design capture. S-Edit is tightly integrated with Tanner EDA's T-Spice™ simulation, L-Edit™ layout, and HiPer™ verification tools.

S-Edit helps you meet the demands of today's fast-paced market by optimizing your productivity and speeding your concepts to silicon. Its efficient design capture process integrates easily with third-party tools. S-Edit enables you to explore design choices and provides an easy-to-use view into the consequences of those choices. A faster design cycle gives you more flexibility in moving to an optimal solution—freeing up more time and resources for process corner validation. The results are less risk downstream, higher yield, and quicker time to market.

### Schematic capture for the most complex full custom IC design

- Bus support speeds the creation of mixed signal designs.
- Advanced array support enables easy creation and editing of memory, imaging, or circuits with repetitive blocks.
- Rubberband connectivity editing enables faster design modifications.
- S-Edit displays evaluated parameters in real time over the course of the design process. Parameters with formulas based on other circuit parameters can be displayed or evaluated.
- Fully configurable auto symbol generation enables you to easily create symbols from schematics, and synchronize any changes.
- All actions are fully scriptable through the TCL/Tk command language.

- Recordable scripts enable you to automate tasks or expand the tool for application-specific needs.
- Replayable logs permit recovery if there is an unexpected network or hardware failure.
- S-Edit performs net highlighting and keeps the net highlighted as you move through the hierarchy.
- Cross probe between schematic, layout, SPICE netlists and LVS results to highlighting nets or devices for easily LVS.
- Schematic ERC enables you to check your design for common errors such as undriven nets, unconnected pins and nets driven by multiple outputs. The design checks are fully configurable, including custom validation scripts.

### Tight integration with simulation

- S-Edit is tightly integrated with simulation. You can drive the simulator from within the schematic capture environment, viewing operating point results directly on the schematic, performing waveform cross-probing to view node voltages and device terminal currents or charges, and easily inspect AC small-signal, model, and device parameters.
- S-Edit creates an efficient flow for the iterative loop of design, simulation, analysis, and tweaking of circuit parameters. The IC designer can focus on the design and not on data processing—thereby speeding up the design process.

### Easy interoperability with third-party tools and legacy data

- S-Edit imports schematics via EDIF from third party tools, including Cadence, Mentor, Laker, ORCAD and ViewDraw with automatic conversion of schematics and properties for seamless integration of legacy data.
- Netlists can be exported in flexible, user-configurable formats, including SPICE and CDL variants, EDIF, structural Verilog, and structural VHDL.
- Library support in S-Edit maximizes the reuse of IP developed in previous projects, or imported from third-party vendors.

### Powerful and easy-to-use interface

- S-Edit brings to front-end design capture the ease-of-use and design productivity for which Tanner Tools™ are known.
- A fully user-programmable design environment allows you to remap hotkeys, create new toolbars, and customize the view to your preference—all in a streamlined GUI.
- The complete user interface is available in multiple Languages and all user data can be entered in international character sets. S-Edit currently supports English, Japanese, Simplified and Traditional Chinese.

### Cost-effective

- S-Edit provides an ideal performance-to-cost ratio, allowing you to maximize the number of designers on a project.
- Since S-Edit runs on Windows® and Linux® platforms, designers can work on cost-effective workstations or laptops. This means you can take your work with you anywhere—even home—and continue working to meet time-to-market pressures.
- Available in two configurations—full schematic editor, and schematic viewer.

### Easy to manage

- Human-readable technology files and design databases are revision-control system-compatible.
- CAD managers can control distribution and access rights to the technology or design. The format allows revision control systems to manage revisions over the course of the design process.

### Benefit from flexible licensing

When you purchase a new design tool, licensing options can greatly affect your total cost of ownership. S-Edit is available in node-locked and networked configurations offering you the most flexible licensing possible. With a single solution, S-Edit will work whenever and wherever meeting the design needs of your main workgroup and remote workers. If you offshore design projects, S-Edit does not have geographic restriction on its licenses, thus, lowering your total cost of ownership.



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Find out what S-Edit can do for your design challenges.  
 Contact us at [DesignChallenge@tannereda.com](mailto:DesignChallenge@tannereda.com) for a 30-day evaluation.

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